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| FORM: PTO-1449<br>(REV. 7-80)   | U.S. DEPARTMENT OF COMMERCE<br>PATENT AND TRADEMARK OFFICE | Atty. Docket No:<br>97-1102.05 | Serial No:<br>09/652,585 |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT<br><br>(37 CFR 1.98(b)) (use several sheets if necessary) |  | Applicant:<br>Akram, et al     |                          |
|   |  | Filing Date:<br>8/31/00        | Group:                   |

## U.S. PATENT DOCUMENTS

| Examiner<br>Initial | Document<br>Number | Date    | Name             | Class | Subclass |  |
|---------------------|--------------------|---------|------------------|-------|----------|--|
| <i>[Signature]</i>  | AA 5,962,921       | 10/5/99 | Farnworth et al. | 257   | 773      |  |
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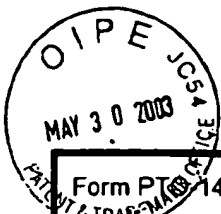
| Examiner<br>Initial | Document<br>Number | Date | Country | Class | Subclass | Translation<br>Yes No                             |
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|                     | AC                 |      |         |       |          | <input type="checkbox"/> <input type="checkbox"/> |

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OTHER ART (including author, title, date, pertinent pages, etc.)

|                    |    |                          |  |
|--------------------|----|--------------------------|--|
| <i>[Signature]</i> | AD |                          | "Condel Test Socket Specifications," Liberty Research Socket Specifications, Web Page: <a href="http://www.liberty-research.com/sktspecs.htm">www.liberty-research.com/sktspecs.htm</a> ; February 10, 1998. |
|                    | AE |                          | "Holeshot Test Socket and Adapter for Area Array Packages," Liberty Research Web Page: <a href="http://www.liberty-research.com/holeshot.htm">www.liberty-research.com/holeshot.htm</a> ; February 10, 1998. |
|                    | AF |                          | "Penta O Probe Sheet," Interconnect Devices, Inc., catalog page.   |
|                    | AG |                          | "Plunger Tip Styles by Size," Catalog page from Interconnect Devices, Inc.   |
|                    | AH |                          | "Probe Plunger Tip Application Guide," Interconnect Devices, Inc., Web Page: <a href="http://www.idinet.com/aguide.htm">www.idinet.com/aguide.htm</a> ; December 20, 1998.                                   |
|                    | AI |                          | "Series 654 Ball Grid Array Socket (on 1.27 mm pitch); catalog page, Wells Electronics, Inc., South Bend, Indiana.   |
|                    | AJ |                          | "Series 654 Ball Grid Array Socket (on 1.5 mm pitch); catalog page, Wells Electronics, Inc., South Bend, Indiana.  |
|                    | AK |                          | "Series 655 OTBGA Burn-In/Test Socket (Pinch Contact)," Wells-CTI, Web Page: <a href="http://www.wellscti.com/ser655p.htm">www.wellscti.com/ser655p.htm</a> ; December 16, 1998.                             |
|                    | AL |                          | "Series 655 OTBGA Socket (Y Contact)," Wells-CTI, Web Page: <a href="http://www.wellscti.com/ser655.htm">www.wellscti.com/ser655.htm</a> ; December 16, 1998.  |
|                    | AM |                          | "Test Sockets and Adapters for Area Array Packages," Liberty Research, Web Page: <a href="http://www.liberty-research.com/arrayinfo.htm">www.liberty-research.com/arrayinfo.htm</a> ; February 10, 1998.     |
| Examiner:          |    | Date Considered: 2/17/00 |  |

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication with applicant.



*A. J. [Signature]*  
(2-92)  
Sheet 1 of 1  
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| Form PT 1449<br><b>INFORMATION DISCLOSURE CITATION<br/>IN AN APPLICATION</b><br>(Use several sheets if necessary) | Docket Number (Optional)<br><b>5095.5US (97-1102.05/US)</b> | Application Number<br><b>09/652,685</b> |
|   | Applicant <b>Akram et al.</b>                               |   |
|   | Filing Date <b>August 31, 2000</b>                          | Group Art Unit <b>3729</b>              |

**U.S. PATENT DOCUMENTS**

| EXAMINER<br>INITIAL | DOCUMENT<br>NUMBER | DATE    | NAME    | CLASS | SUBCLASS | FILING DATE<br>IF APPROPRIATE |
|---------------------|--------------------|---------|---------|-------|----------|-------------------------------|
| <i>[Signature]</i>  | 5,741,741          | 04/1998 | Tseng   |       |          |                               |
|                     | 6,049,977          | 04/2000 | Atkins  |       |          |                               |
|                     | 6,092,282          | 07/2000 | Roberts |       |          |                               |
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**FOREIGN PATENT DOCUMENTS**

|  | DOCUMENT<br>NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | Translation |    |
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|  |                    |      |         |       |          | YES         | NO |
|  |                    |      |         |       |          |             |    |
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|  |                    |      |         |       |          |             |    |
|  |                    |      |         |       |          |             |    |

**OTHER DOCUMENTS**

(Including Author, Title, Date, Pertinent Pages, Etc.)

|                    |  |  |
|--------------------|--|--|
| <i>[Signature]</i> |  | Smith, "Circuits, Devices and Systems" Fourth Edition, 1984. |
|                    |  |  |
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| EXAMINER<br><i>[Signature]</i> | DATE CONSIDERED<br><i>4/7/05</i> |
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